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N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ohtani et al.

Art Unit : 2811

Serial No. : 08/958,568

Examiner : Hu, S.

Filed

: October 28, 1997

Title

: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING T

SAME

Commissioner for Patents Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information of Disclosure Statement and documents listed on form PTO-1449.

Kindly accept this Information Disclosure Statement under Rule 97(c)(2). The rule 17(p) certification fee of \$180 is enclosed.

The examiner's attention is drawn to the fact that, in addition to the abstracts provided with the Japanese patents, Document AA is a patent family member of Documents AL, AM and AN; and Document AB is patent family member of Document AO.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit

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Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 21/03

Scott C. Harris Reg. No. 32,030

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